

10529175
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A Wright
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In re Appln. of TOKIOKA et al.
Application No. Unassigned

SPECIFICATION AMENDMENTS

*ANW
2/28/08*
Replace the paragraph beginning at page 3, line 18 with:

A current supply circuit according to another arrangement of the present invention is a current supply circuit providing an output current corresponding to digital data of n bits (n is an integer not less than 2), including: a current output node electrically connected to a first power supply node via a current driving device during current supply; a current control circuit provided between a second power supply node and the current output node and receiving the digital data for controlling, corresponding to the digital data, an amount of current on a current path established including the ~~current-data line~~ output node between the first and second power supply nodes during the current supply; and a current regulating circuit receiving the digital data for moving, prior to the current supply, a voltage on the current output node closer to a voltage corresponding to the digital data.